

*OUTPUTS*

• *A* •                      • *B* •

*control voltage*                      *INPUTS*                      *control voltage*



*zero*

*max*

*zero*

*max*

*OUTPUTS*

• *C* •                      • *D* •

*control voltage*                      *INPUTS*                      *control voltage*



*zero*

*max*

*zero*

*max*