



Yusynth Clockdivider

<http://yusynth.net/Modular/EN/DIVIDER/index.html>

Vero Board

- C1, C2 10uF
- C3, C4, C5 100nF
- C6 1nF
- D1, D2, D3, D4, D5, D6, D7, D8, D9, D10, D11, D12, D13, D14, D15, D16 1N4148
- Eyelet M3
- Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, Q10, Q11 BC547C
- R1, R2 10Ω
- R3, R15, R27, R40 1M
- R4, R16, R28 1,2M

- R5, R12, R17, R25, R29, R36 220Ω
- R6, R7, R9, R18, R19, R21, R30, R31, R33, R42, R43 22K
- R8, R14, R20, R26, R32, R38 1K
- R10, R22, R34 4,7K
- R11, R23, R35 220K
- R13, R25, R37 1,5K
- R39 100K
- R41 10K
- U1, U2, U3 CD4017

- 220Ω
- 22K
- 1K
- 4,7K
- 220K
- 1,5K
- 100K
- 10K
- CD4017

Switches, Jacks and LEDs not shown

Optional Zener Diodes would have to be mounted on top of R13, R25, R37

Modifications

A. Extended (up to 10 div) version shown. If not needed refer to original schematic/wiring diagram.

B. For reset on rising clock (not shown):

- 1) Replace Q11 with BC557C (PNP)
- 2) Rotate Q11 by 180° (i.e. swap emitter and collector)
- 3) Connect base of Q11 with R42 to Vcc+ (instead to ground)

<http://electro-music.com/forum/viewtopic.php?t=69933&sid=ed81d4debc0b41cc8dea4d2374f4fc89>

created with DIY Layout Creator G3 V40.0

helix modular 2018-06-03

