



**Yusynth Dual Gate Delay**

<http://yusynth.net/Modular/EN/GATEDELAY/index.html>

- C1, C2 22uF
- C3, C5 1uF
- D1, D2, D3, D4 1N4148
- Eyelet M3
- Q1, Q2, Q5, Q6 BC547C
- Q3, Q7 BS170
- Q4, Q8 BS250
- R1, R2 10Ω
- R4, R7, R12, R13, R14, R15, R18, R23, R24, R25 1K
- R5, R6, R16, R17 22K
- R8, R19 5.1K
- R9, R11, R20, R22 10K
- R10, R21 2.7K
- Vero Board 18 x 24-27
- U1, U2 TL072

Not shown: P1, P2, C4, C6

NB: C4,C6 100n are mounted on the backside (decoupling U1-U2)

R4, R15 may be increased to 10k to prevent excess current draw from GATE IN

created with DIY Layout Creator G3 V 40.0

helix modular 2018-03-18