



Yusynth Min Max + Inverter

<http://yusynth.net/Modular/EN/MINMAX/index.html>

- C1, C2
- Cxx, Cxx
- D1, D2, D3, D4
- Eyelet
- R1, R2
- R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R14, R15, R16, R17, R18, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R33, R34, R35, R36, R37, Rx, Rx, Rx, Rx
- R13, R19, R20, R21, R32, R38, R39, R40
- Ry, Ry, Ry, Ry, Ry, Ry
- Vero Board
- U1, U2
- U3ab, U3cd, Ux

- 22uF
- 47pF
- 1N4148
- M3
- 10Q
- 100K
- 50K
- 100Q
- 18 x 39
- TL074
- TL072

Not shown: C3, C4, C5c, C5b, Cx (100n) are mounted on the back
 Note: 50k = 100k || 100k

Modifications

- Split U3 in two opamps (one for each channel)
- Added 100Ohm output resistors for opamp protection (Ry)
- Compenets of optional inverter section marked with 'x'
- Cxx prevents oscillation of Ux. Cxx: 47 pF range